

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Michitaka KAMEYAMA et al.

Serial No: 10/502,265

Confirmation No.: 9388

Filed: July 22, 2004

For: LOGICAL OPERATION CIRCUIT
AND LOGICAL OPERATION
METHOD

Art Unit: 2819

Examiner: Vibol Tan

DECLARATION UNDER 37 CFR 1.132

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

1. I am Yoshikazu Fujimori, one of the inventors of the present application.

I began working for Rohm Co., Ltd., on Apr. 1, 1996.

I hold the position of senior engineer.

My main area of research is semiconductor devices.

I have earned the following degrees: Doctor of engineering.

2. I am advised that in an Office Action dated August 29, 2006, Claims 1-3, 8-11 and 14 were rejected under 35 U.S.C. 102(e) as being anticipated by US Patent Application Publication No. 2005/0017757 (Fujimori), which was first filed in the U.S. on July 12, 2004.

3. I am the named inventor of the US 2005/0017757 patent application.

4. The subject matter claimed in Claims 1-3, 8-11 and 14 of the present application, and the same subject matter to the extent it is described in the US 2005/0017757 Fujimori patent application, is based on my work.

5. I declare under penalty of perjury that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true.

Respectfully submitted,

Yoshikazu Fujimori

Yoshikazu Fujimori

Dated: *Aug. 23, 2007*